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 $\succ$

A diagram of a 10-bit shift register. It consists of two horizontal rows of five rectangular cells each. The top row is labeled 'Q9' through 'Q4' from left to right. The bottom row is labeled 'Q3' through 'Q0' from left to right. Vertical lines connect the cells of the top row to the cells of the bottom row. Horizontal lines connect the cells within each row. There are also horizontal lines extending from the left and right ends of the bottom row, indicating data input and output.

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